

REMARKS

Claims 1-28 are pending. Claims 1-6 and 9-28 are rejected under 35 U.S.C. § 102(a). Claims 7-8 are objected to. Claims 1 and 24 are amended.

The specification has been amended to correct a numerical error in applicant's admitted prior art. Referring to Figure 1, sub-window 20, having a 66.7 μsec duration, discloses 256 chips and 512 sample positions. Thus, time slot 10, having a 667 μsec duration (page 5, lines 8-9), has 2560 chips and 5120 sample positions. No new matter is added.

Examiner requires a drawing correction to label Figure 1 Prior Art. The proposed drawing amendment is attached. This correction is also being separately mailed on formal drawings.

Independent claims 1 and 24 are rejected as being anticipated by applicant's admitted prior art. Examiner correctly notes that applicant's admitted prior art discloses a time slot window 10 and a search window 20. The search window 20 is typically on the order of one-tenth the duration of the time slot 10. Claim 1, as amended, recites "A wireless receiver, comprising: at least one antenna for receiving a plurality of frames in a form of a plurality of paths; wherein each of the plurality of frames comprises a plurality of time slots; wherein each of the plurality of time slots comprises a plurality of symbols; wherein each of the plurality of paths has a corresponding sample position; and wherein the plurality of symbols comprise a primary synchronization code symbol; circuitry for correlating a primary synchronization code across a group of the plurality of symbols; circuitry for identifying a plurality of path positions within the group, wherein each of the plurality of path positions corresponds to a respective one of a plurality of largest-amplitude paths represented within the group as detected in response to the circuitry for correlating; *circuitry for defining a plurality of sub-windows within one of the plurality of time slots.*" (emphasis added). Claim 24, as amended, recites "A method of operating a wireless receiver, comprising the steps of: receiving a plurality of frames in a form of a plurality of paths along at least one antenna; wherein each of the plurality of frames comprises a plurality of time slots; wherein each of the plurality of time slots

comprises a plurality of symbols; wherein each of the plurality of paths has a corresponding sample position; and wherein the plurality of symbols comprise a primary synchronization code symbol; correlating a primary synchronization code across a group of the plurality of symbols; identifying a plurality of path positions within the group, wherein each of the plurality of path positions corresponds to a respective one of a plurality of largest-amplitude paths represented within the group as detected in response to the circuitry for correlating; *defining a plurality of sub-windows within one of the plurality of time slots.*" (emphasis added).

The invention of claims 1-28, defines a plurality of sub-windows 92<sub>1</sub>-92<sub>4</sub> within a single time slot as shown in Figure 6. These sub-windows are defined by circuit 56 (Figure 4) as described at step 56<sub>ml</sub> (Figure 5) at page 16, lines 14-15. These sub-windows in a single time slot are not disclosed in applicant's admitted prior art. Moreover, the present invention is advantageously compared to the prior art at page 19, lines 1-24. Therein, a preferred embodiment of the present invention details a 97% reduction in computational complexity. Thus, applicant respectfully submits that claims 1-28, as amended, are patentable in view of admitted prior art.

In view of the foregoing, applicant respectfully requests reconsideration and allowance of claims 1-28. If the Examiner finds any issue that is unresolved, please call applicant's attorney by dialing the telephone number printed below.

Respectfully submitted,



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